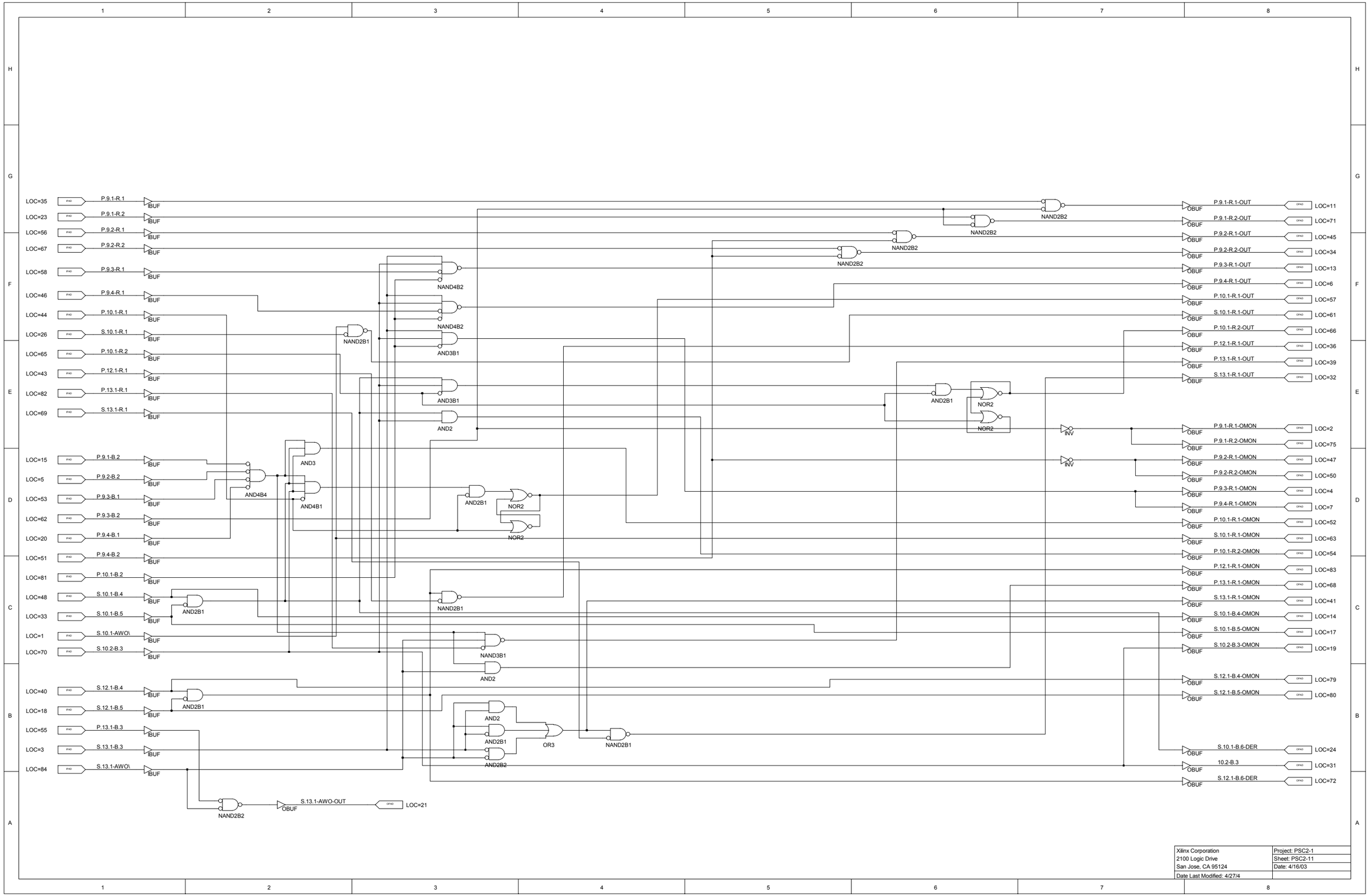
LOC=35 P.9.1-R.1 IBUF
LOC=23 P.9.1-R.2 IBUF
LOC=56 P.9.2-R.1 IBUF
LOC=67 P.9.2-R.2 IBUF
LOC=58 P.9.3-R.1 IBUF
LOC=46 P.9.4-R.1 IBUF
LOC=44 P.10.1-R.1 IBUF
LOC=26 S.10.1-R.1 IBUF
LOC=65 P.10.1-R.2 IBUF
LOC=43 P.12.1-R.1 IBUF
LOC=82 P.13.1-R.1 IBUF
LOC=69 S.13.1-R.1 IBUF
LOC=15 P.9.1-B.2 IBUF
LOC=5 P.9.2-B.2 IBUF
LOC=53 P.9.3-B.1 IBUF
LOC=62 P.9.3-B.2 IBUF
LOC=20 P.9.4-B.1 IBUF
LOC=51 P.9.4-B.2 IBUF
LOC=81 P.10.1-B.2 IBUF
LOC=48 S.10.1-B.4 IBUF
LOC=33 S.10.1-B.5 IBUF
LOC=1 S.10.1-AWO\ IBUF
LOC=70 S.10.2-B.3 IBUF
LOC=40 S.12.1-B.4 IBUF
LOC=18 S.12.1-B.5 IBUF
LOC=55 P.13.1-B.3 IBUF
LOC=3 S.13.1-B.3 IBUF
LOC=84 S.13.1-AWO\ IBUF
NAND2B2
NAND2B2
NAND2B2
NAND2B2
NAND4B2
NAND4B2
NAND2B1
AND3B1
AND3B1
AND2
AND3
AND4B4
AND4B1
AND2B1
NOR2
NOR2
AND2B1
NOR2
NOR2
INV
INV
AND2B1
NAND2B1
AND2B1
NAND3B1
AND2
AND2B1
AND2
AND2B1
AND2B2
OR3
NAND2B1
NAND2B2
S.13.1-AWO-OUT OBUF LOC=21
P.9.1-R.1-OUT OBUF LOC=11
P.9.1-R.2-OUT OBUF LOC=71
P.9.2-R.1-OUT OBUF LOC=45
P.9.2-R.2-OUT OBUF LOC=34
P.9.3-R.1-OUT OBUF LOC=13
P.9.4-R.1-OUT OBUF LOC=6
P.10.1-R.1-OUT OBUF LOC=57
S.10.1-R.1-OUT OBUF LOC=61
P.10.1-R.2-OUT OBUF LOC=66
P.12.1-R.1-OUT OBUF LOC=36
P.13.1-R.1-OUT OBUF LOC=39
S.13.1-R.1-OUT OBUF LOC=32
P.9.1-R.1-OMON OBUF LOC=2
P.9.1-R.2-OMON OBUF LOC=75
P.9.2-R.1-OMON OBUF LOC=47
P.9.2-R.2-OMON OBUF LOC=50
P.9.3-R.1-OMON OBUF LOC=4
P.9.4-R.1-OMON OBUF LOC=7
P.10.1-R.1-OMON OBUF LOC=52
S.10.1-R.1-OMON OBUF LOC=63
P.10.1-R.2-OMON OBUF LOC=54
P.12.1-R.1-OMON OBUF LOC=83
P.13.1-R.1-OMON OBUF LOC=68
S.13.1-R.1-OMON OBUF LOC=41
S.10.1-B.4-OMON OBUF LOC=14
S.10.1-B.5-OMON OBUF LOC=17
S.10.2-B.3-OMON OBUF LOC=19
S.12.1-B.4-OMON OBUF LOC=79
S.12.1-B.5-OMON OBUF LOC=80
S.10.1-B.6-DER OBUF LOC=24
10.2-B.3 OBUF LOC=31
S.12.1-B.6-DER OBUF LOC=72
Xilinx Corporation
2100 Logic Drive
San Jose, CA 95124
Date Last Modified: 4/27/4
Project: PSC2-1
Sheet: PSC2-11
Date: 4/16/03